

Notice of References Cited	Application/Control No. 10/750,154	Applicant(s)/Patent Under Reexamination BAINS ET AL.	
	Examiner Pierre-Michel Bataille	Art Unit 2186	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Saulsbury, A., et al., "Missing th Memory Wall: The Case for Processor/Memory Integration", Proceedings of the 23rd Annual Intl. Symposium on Computer Architecture, vol. 24(2):90-101 (May 1996).
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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